FIGURE 1

100

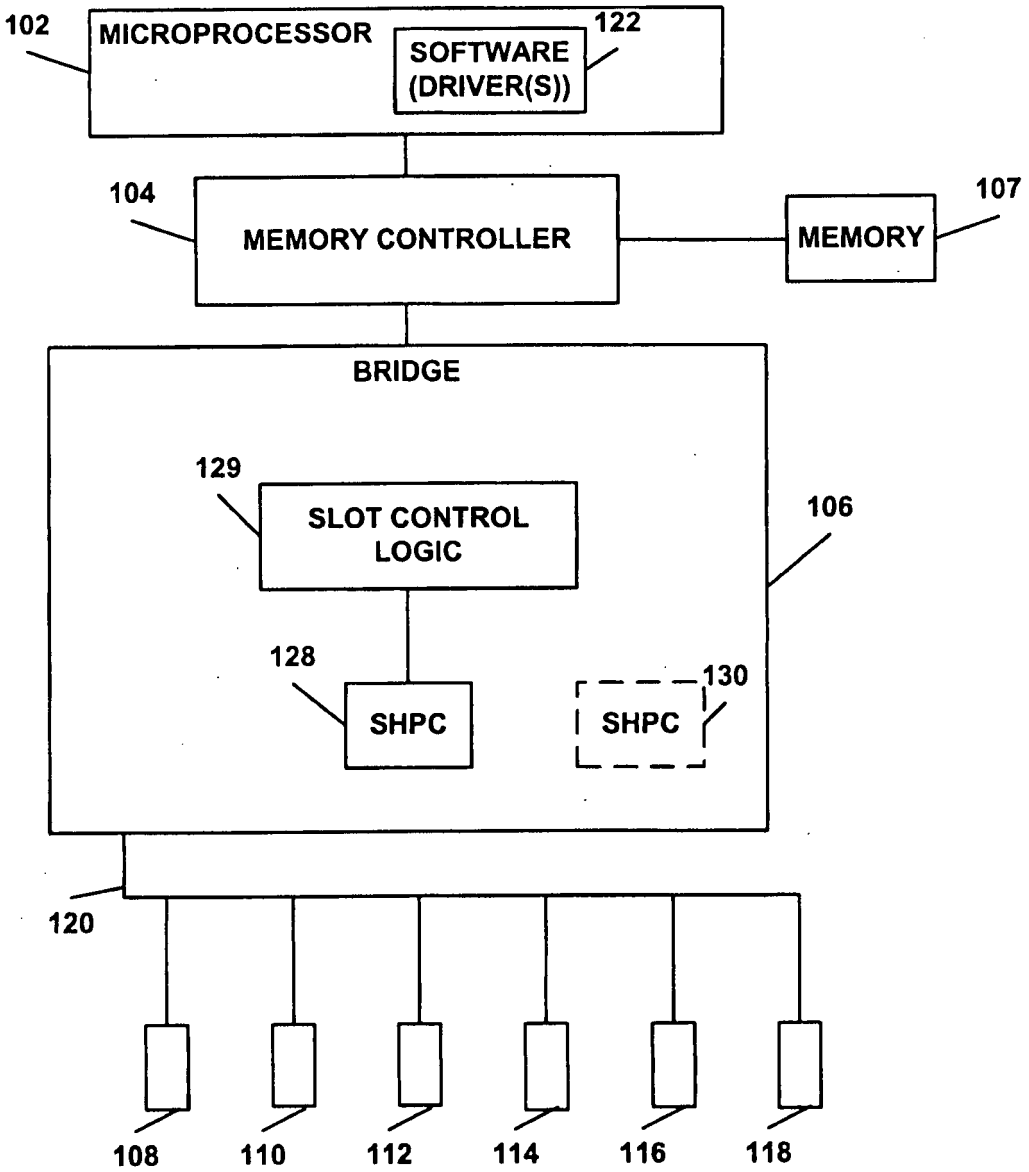

102
MICROPROCESSOR
122
SOFTWARE
(DRIVER(S))
104
MEMORY CONTROLLER
107
MEMORY
BRIDGE
129
SLOT CONTROL
LOGIC
106
128
SHPC
130
SHPC
120
108
110
112
114
116
118

FIGURE 2

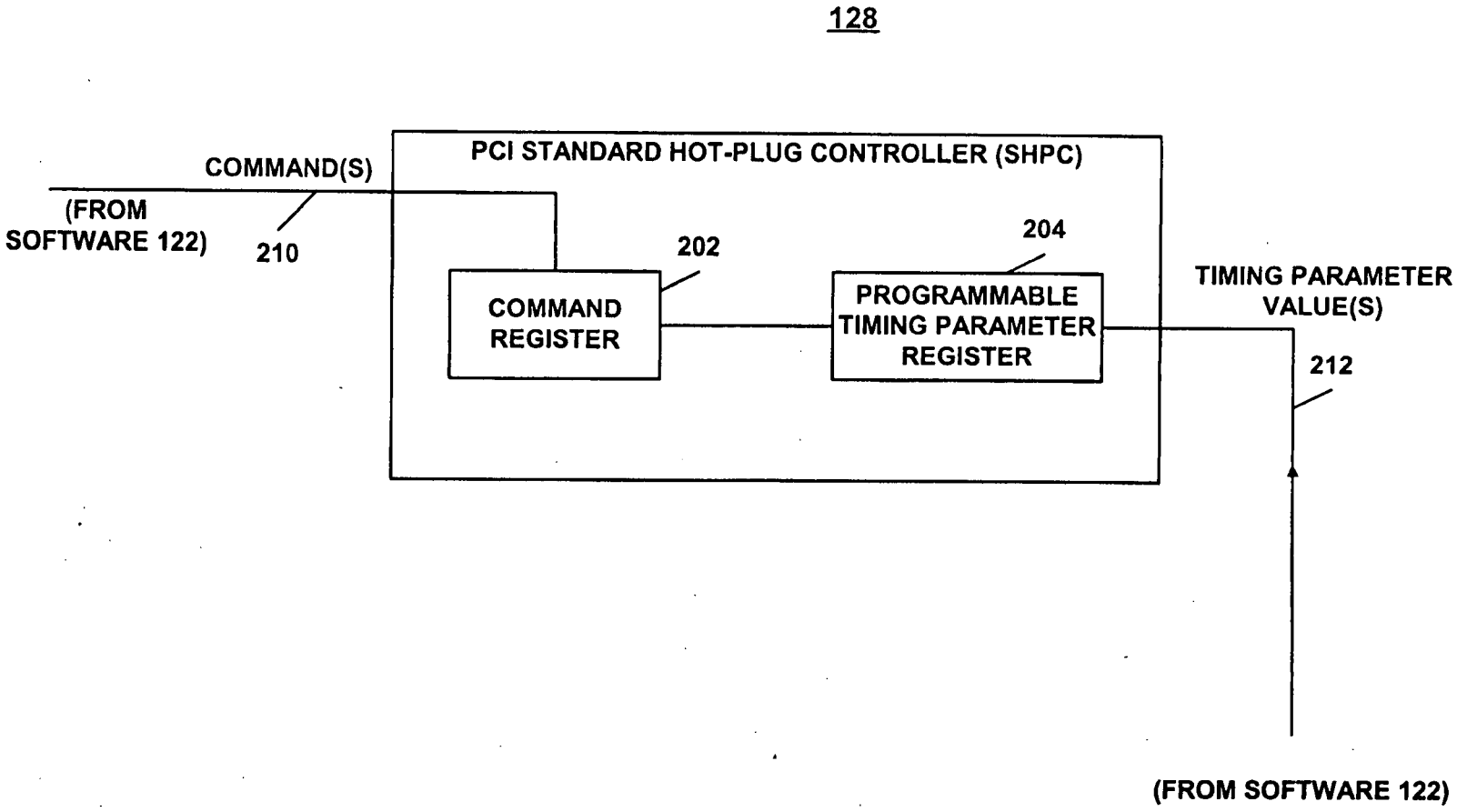
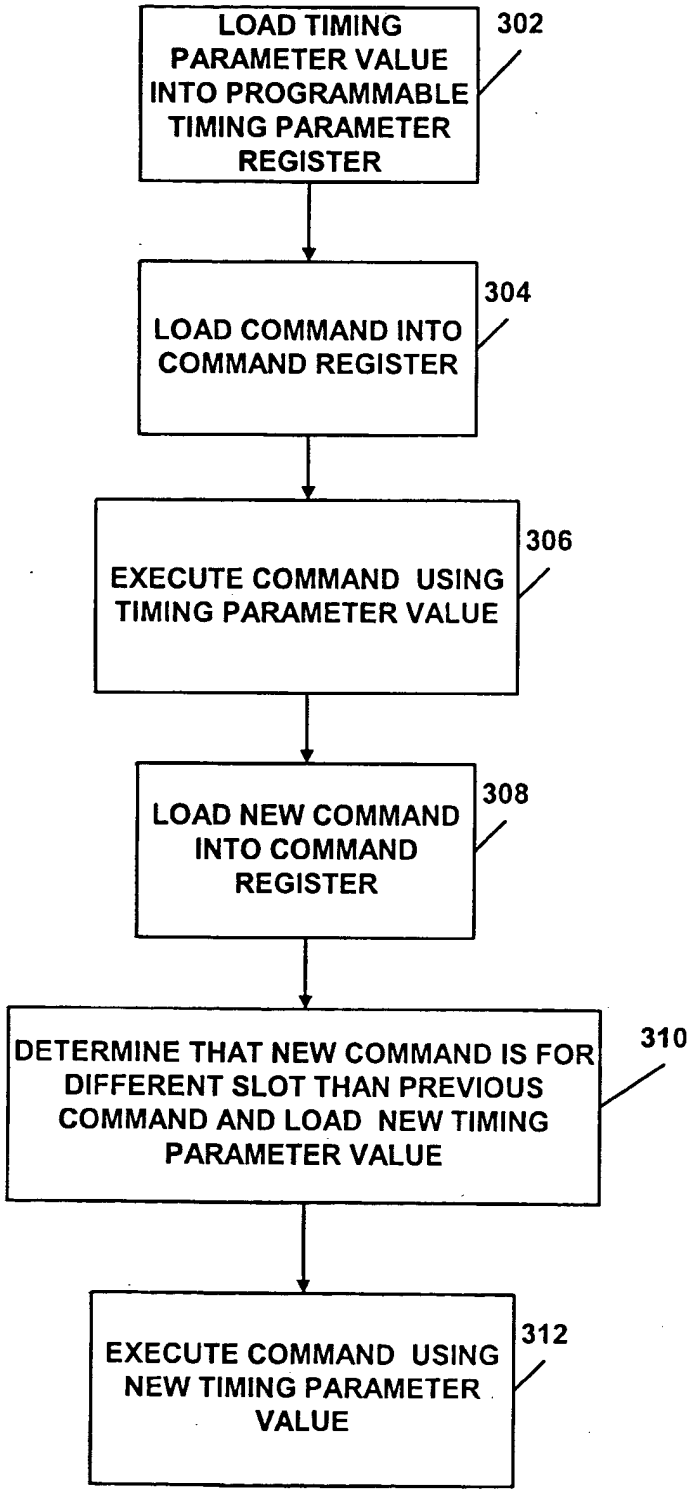

128
PCI STANDARD HOT-PLUG CONTROLLER (SHPC)
COMMAND(S)
(FROM SOFTWARE 122)
210
202
COMMAND REGISTER
204
PROGRAMMABLE TIMING PARAMETER REGISTER
TIMING PARAMETER VALUE(S)
212
(FROM SOFTWARE 122)

FIGURE 3

300


LOAD TIMING PARAMETER VALUE INTO PROGRAMMABLE TIMING PARAMETER REGISTER
302
LOAD COMMAND INTO COMMAND REGISTER
304
EXECUTE COMMAND USING TIMING PARAMETER VALUE
306
LOAD NEW COMMAND INTO COMMAND REGISTER
308
DETERMINE THAT NEW COMMAND IS FOR DIFFERENT SLOT THAN PREVIOUS COMMAND AND LOAD NEW TIMING PARAMETER VALUE
310
EXECUTE COMMAND USING NEW TIMING PARAMETER VALUE
312

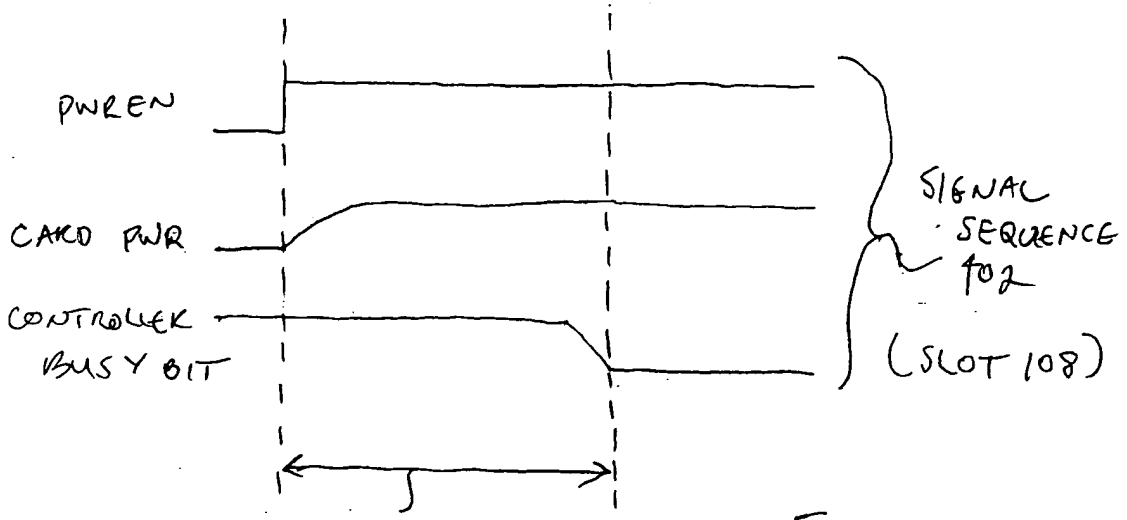


Figure 4a

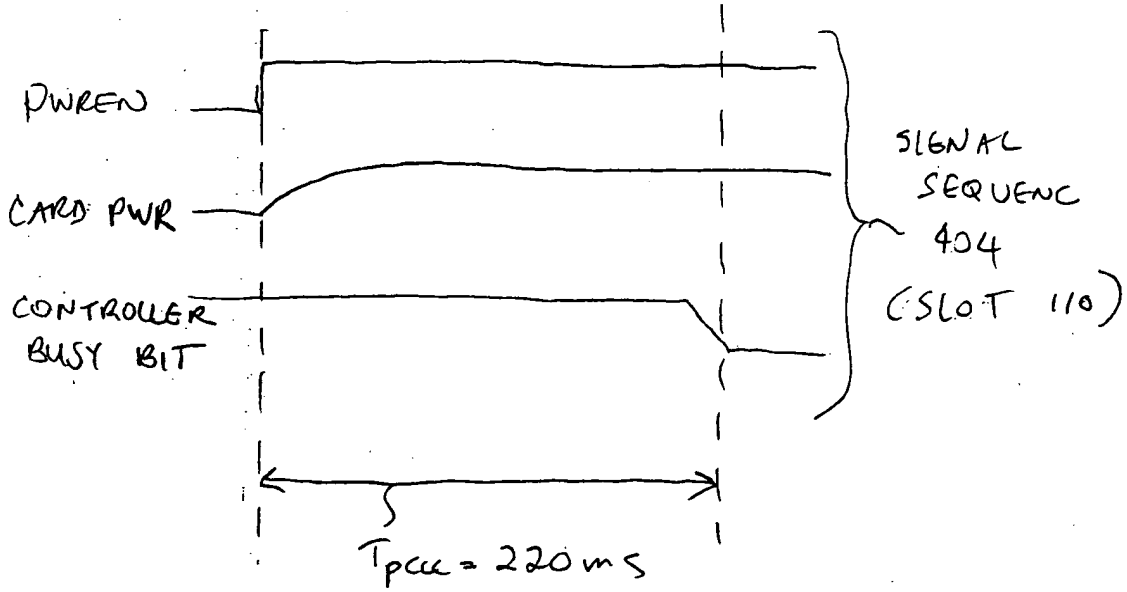
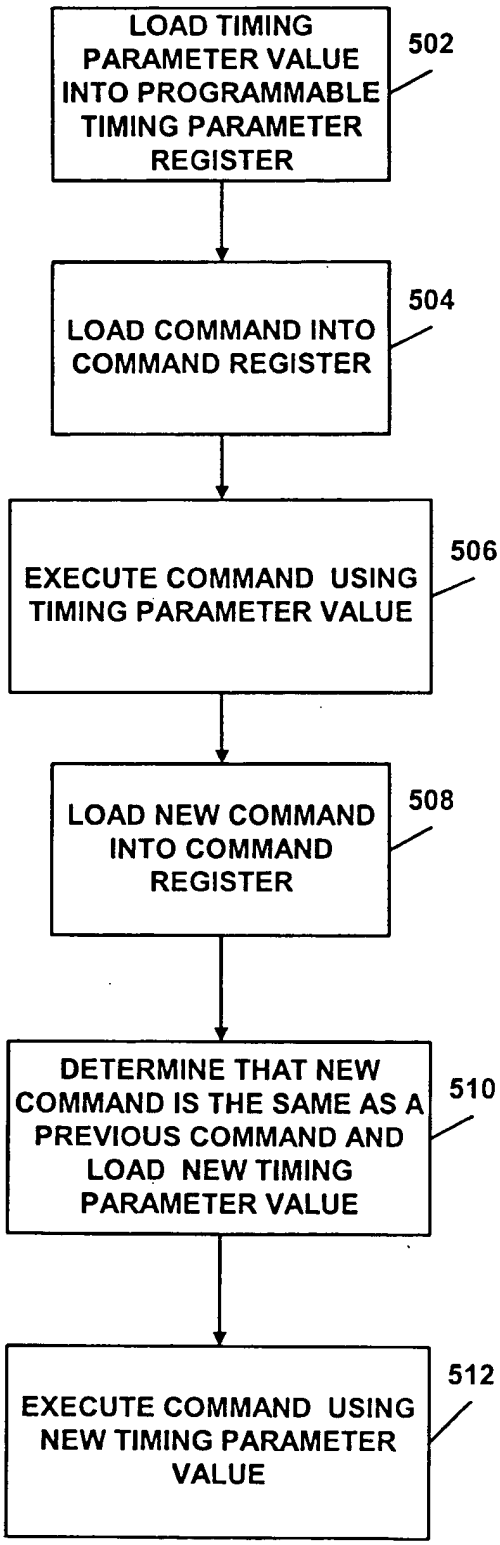


FIGURE 4b

FIGURE 5

500


LOAD TIMING PARAMETER VALUE INTO PROGRAMMABLE TIMING PARAMETER REGISTER
502
LOAD COMMAND INTO COMMAND REGISTER
504
EXECUTE COMMAND USING TIMING PARAMETER VALUE
506
LOAD NEW COMMAND INTO COMMAND REGISTER
508
DETERMINE THAT NEW COMMAND IS THE SAME AS A PREVIOUS COMMAND AND LOAD NEW TIMING PARAMETER VALUE
510
EXECUTE COMMAND USING NEW TIMING PARAMETER VALUE
512

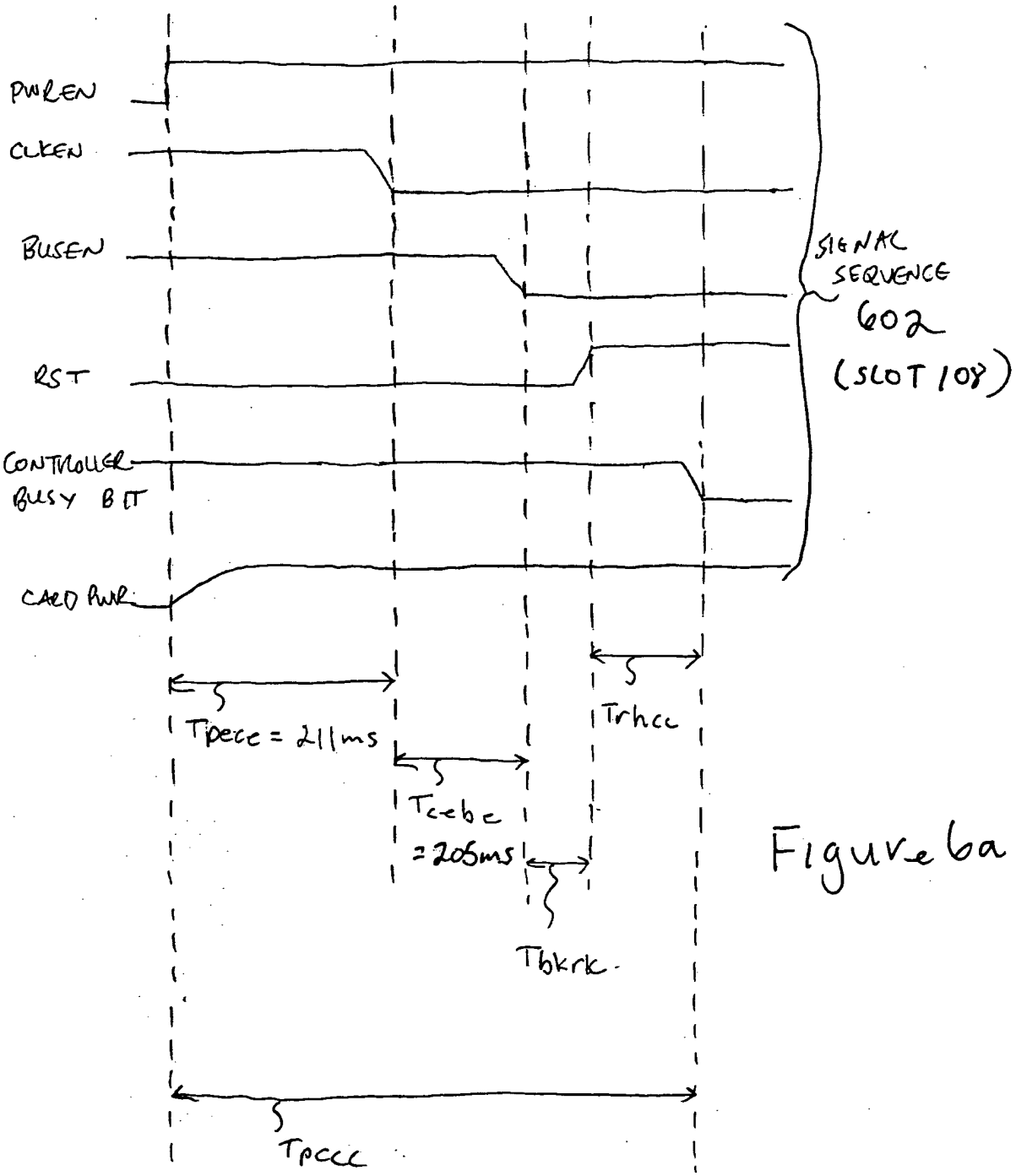

PWREN
CLKEN
BUSEN
RST
CONTROLLER BUSY BIT
CARD PWR
SIGNAL SEQUENCE 602 (SLOT 108)
Tpecc = 211ms
Tcebc = 205ms
Tbkrk
Trhcc
Tpccc

Figure 6a

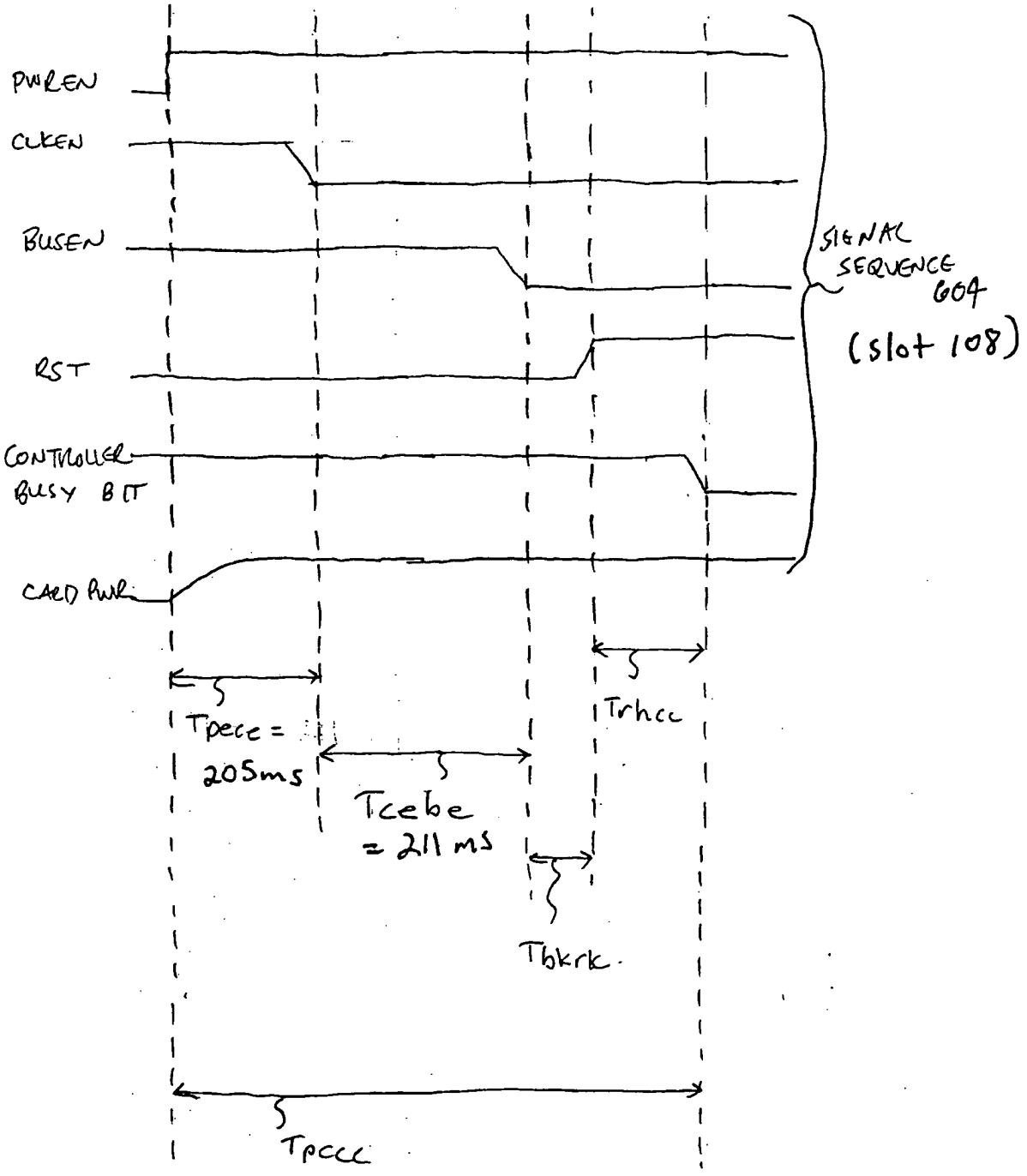



Figure 6b 600